Dynamic Reconfigurable RF Circuit Design

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Multi-band RF Transceiver





On-chip RF Transceiver with Direct-Conversion Architecture

Multi-band RF Transceiver

We propose Reconfigurable RF Circuit Design for multi-band RF Circuits.

2. Reconfigurable RF Circuit Design

Reconfigurable RF Circuit

Multi function

Discrete (switching) Dual-band, Quad-band, Dual-mode,...

Continuous

Multi-band, Multi-mode

Self compensation

Static

Process fluctuation, Design error, Yield enhancement Simulation error, ...

Dynamic

Temperature, Noise, Power supply, ...

Convenience, Downsizing, ...

Control

Digital Circuit



4. Wide Tuning Range LC-VCO

LC-VCO Using Variable Inductor



The proposed architecture **Simulation Results**

Reconfigurable Phase Locked Loop



3. Dynamic Self Reconfiguration





2.4GHz to 3.2GHz (Tuning range of 29%) Power Consumption: 2.7mW

1.1GHz to 2.6GHz (Tuning range of 80%) Power Consumption: 20mW (maximun)

VCO micrograph

Comparison of Wide Tuning Range VCO Architecture

VCO Architecture		Tuning Range	Power Consumption	Phase Noise
1 Ring Oscillator		very wide	small	bad
LC-VCO	2 Varactor	narrow	not large	very good
	③Switched Capacitor	wide	large	good
	4 Variable Inductor	wide	not large	very good
	⑤Variable Inductor+ Switched Capacitor	very wide	large	good

LC-VCO with variable inductor and switched capacitor provides 80% of tuning range and good phase noise characteristic.

5. Summary

Reconfigurable RF circuit architecture was proposed.

Multi-function and Self compensation

Multi-band/mode Transceiver, Convenience, Downsizing, ... Dynamic power reduction, Yield enhancement, ...