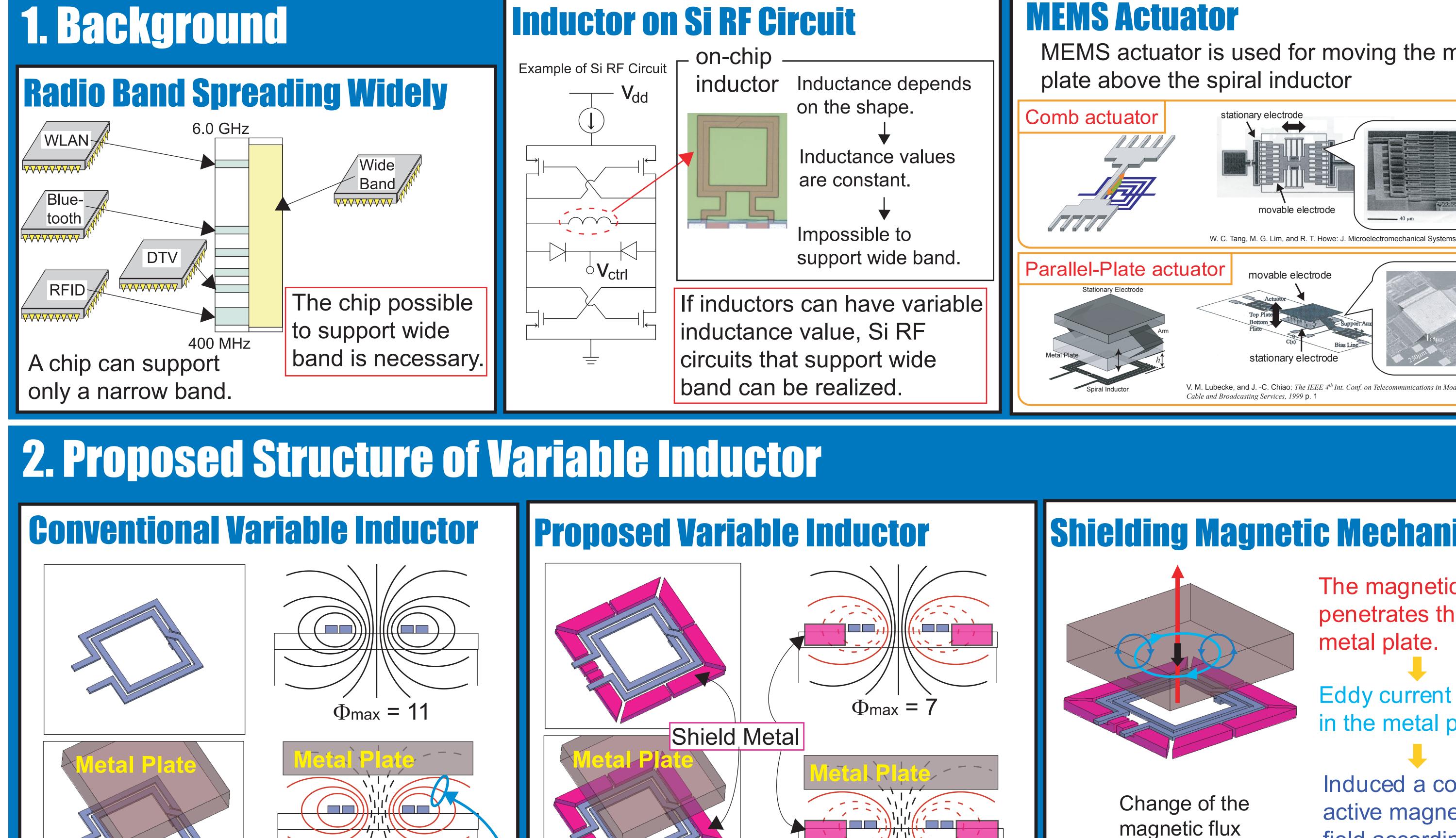
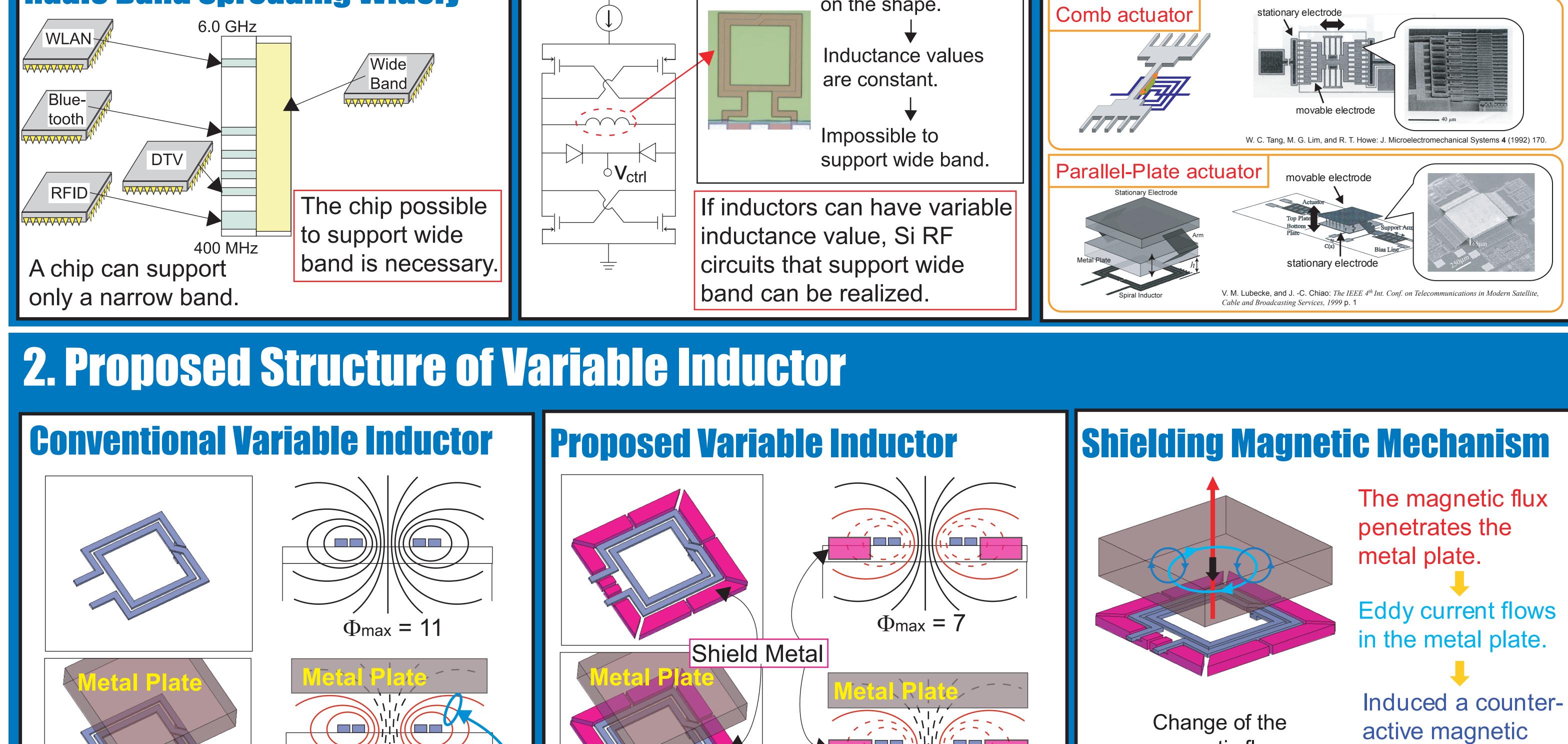
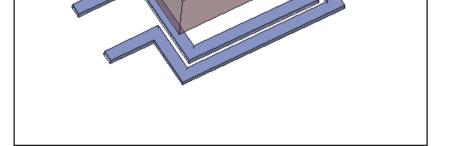
# A Large Variable Ratio On-Chip Inductor with Spicer Legs Shield Tackya Yammouch, Hirotaka Sugawara, Kenichi Okada, and Kazuya Masu Precision and Intelligence Laboratory, Tokyo Institute of Technology, Japan

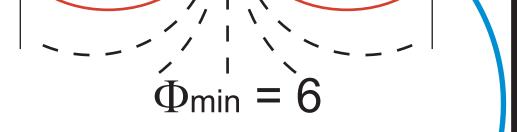


#### **MEMS Actuator**

MEMS actuator is used for moving the metal

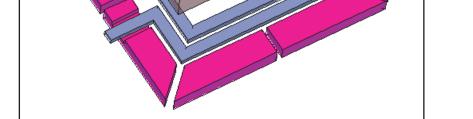






Metal plate cancels magnetic flux. But...

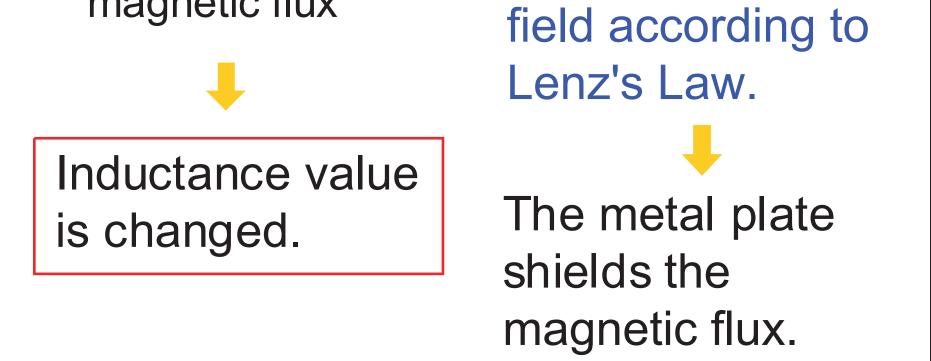
Some of fluxes cannot be canceled.



Shield metals cancel the undesired flux.

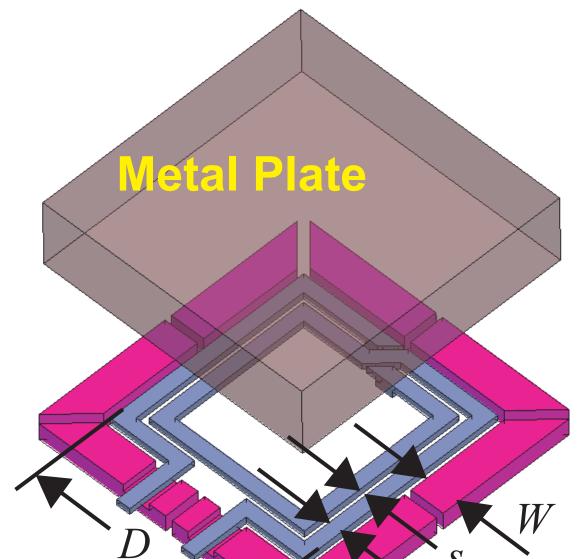
 $\Phi$ min

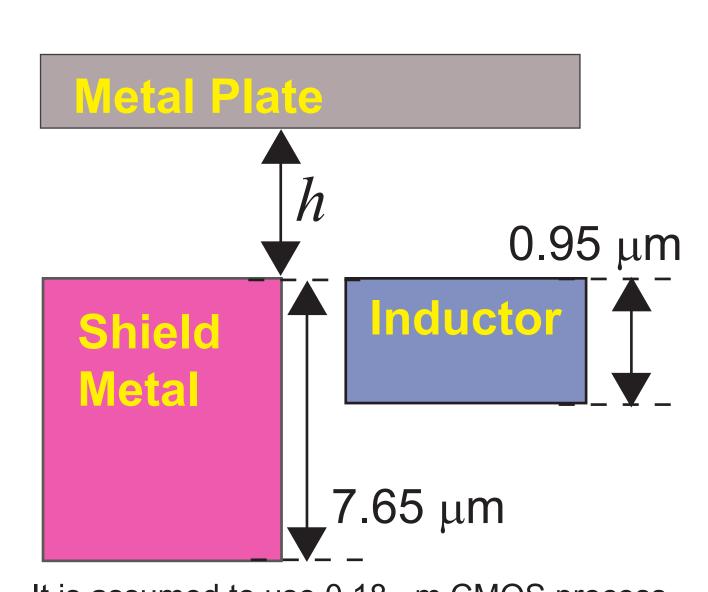
 $\rightarrow \Phi_{max}/\Phi_{min}$  increases. Thus, variable ratio increases.

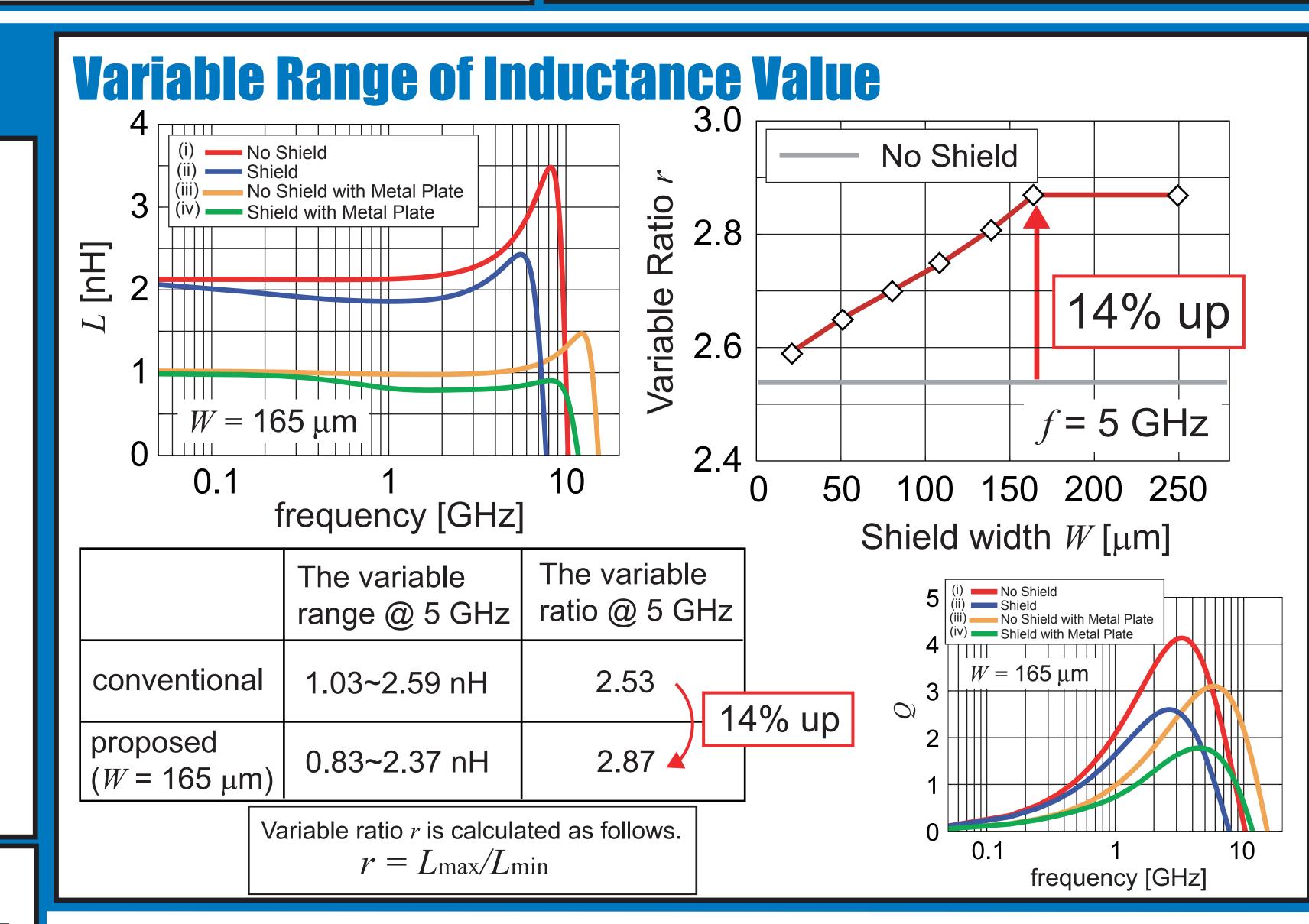


## 3. Result of Simulation

## **Simulation Method**





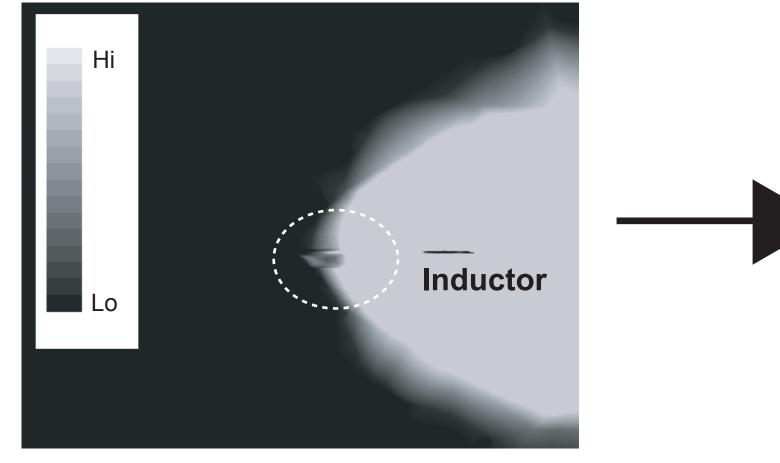


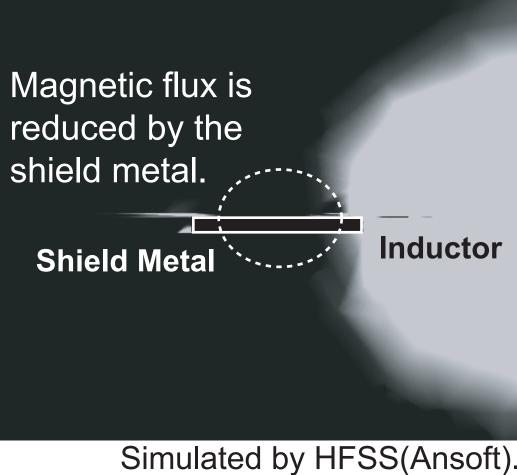


outer diameter  $D = 300 \,\mu\text{m}$ line width  $W = 20 \ \mu m$ line space  $s = 1.2 \,\mu m$ shield width  $W = 20 \sim 250 \,\mu m$ 

It is assumed to use 0.18  $\mu$ m CMOS process. Shield metals consist of 5 layers from M5 to M1. The inductor is made in M5. Simulation is performed by HFSS(Ansoft). height of metal plate  $h = 10 \ \mu m \text{ or } \infty$ 

### **Magnetic Field around the Inductor**





## 4. Conclusion

- The maximum variable ratio of inductance is 2.87 @ 5 GHz
- The variable ratio increases 14% @ 5 GHz

The proposed variable inductor can be applied to Si CMOS RF circuits, and improves the tuning range of circuits.