

微細集積回路の設計自動化技術

Variation-aware electrical design automation



東京工業大学 統合研究院 益研究室
Tokyo Institute of Technology, Masu Group
<http://www.masu-lab.com/>

目的, Purpose

継続的微細化を実現する設計技術の研究

- ばらつき要因の統計学的考察
- 統計的回路解析・最適化技術
- 微細化に適する回路性能向上技術

Design methodologies for quality-enhancing miniaturization

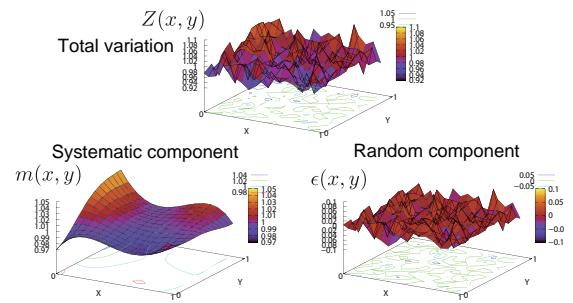
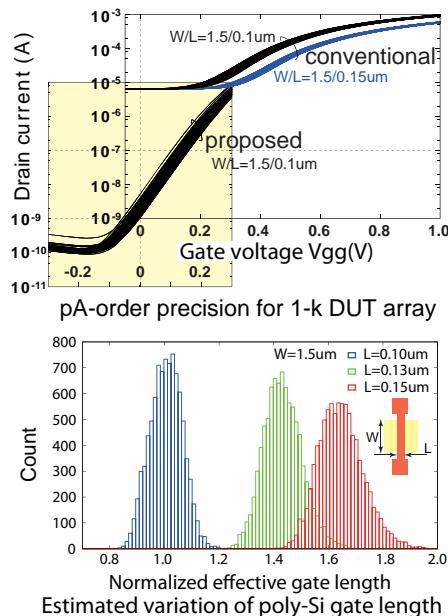
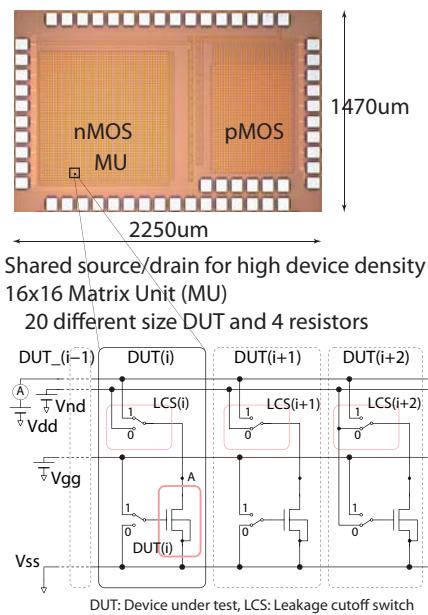
- Statistical device modeling under process variation
- Statistical analysis and optimization
- Variation-aware circuit design

ハイライト, Highlights

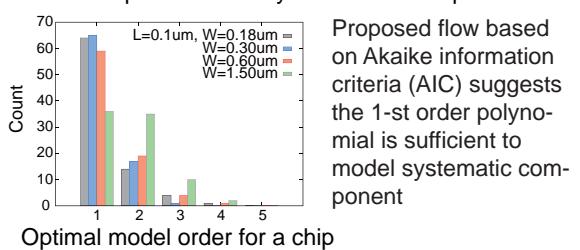
- トランジスタアレイによる微小漏れ電流／チャネル長の高精度測定と大域・局所ばらつき成分の最適分離
- ノンパラメトリック統計による任意分布統計的タイミング解析
- Array based sub-threshold current / channel-length measurement and optimal systematic trend determination
- Non-parametric statistical static timing analysis for arbitrary delay distribution

詳細技術, Details

Array-based device-variation evaluation



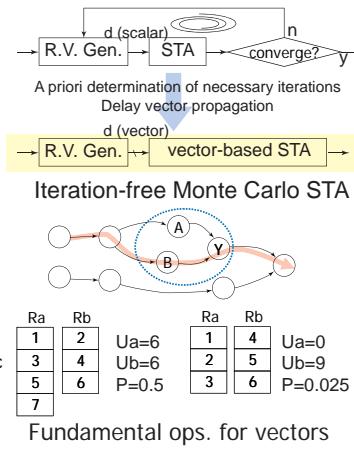
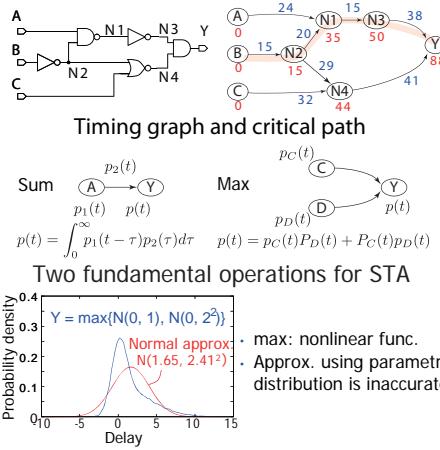
Variation decomposition for accurate modeling
Too complex model may result in overfit problem



Proposed flow based on Akaike information criteria (AIC) suggests the 1-st order polynomial is sufficient to model systematic component

Optimal model order for a chip

Non-parametric statistical static timing analysis



	Determ. STA	Statistical	
		Conventional STA	Non-parametric STA
Delay repr.	Deterministic val.	Parametric probability distribution	Delay vector
Fund. op.	max	Scalar max	Gaussian that matches both mean and variance to $\max(A, B)^{1/2}$
	sum	Scalar sum	Vector selection by Mann-Whitney U
Criticality		Tightness ²⁾ , etc.	Mann-Whitney U, P-value
Memo		<ul style="list-style-type: none">Normal distributionCanonical delay modelRepetition of 2-input procedure	<ul style="list-style-type: none">Arbitrary distributionArbitrary delay modelSingle procedure for multiple input