



IEEE

Solid-State Circuits Society
Japan Chapter



VDEC LSI Designers Forum
IEEE SSCS Japan Chapter
Best Poster Award

is presented to

Shiho Hagiwara

for the poster entitled

「電源遮断回路における製造ばらつきの
パス遅延時間への影響」

at VDEC LSI Designers Forum 2008
in Tokyo, June 6, 7 and 8, 2008.

Tohru Furuyama
Chair 2008

June 7, 2008.